

CLAIMS

What is claimed is:

1. An apparatus, comprising:

a standard hot-plug controller, the standard hot-plug controller having a register for receiving at least one command from a microprocessor, the standard hot-plug controller to:

cause execution of a blinking pattern on at least one light emitting diode associated with at least one target peripheral card interconnect slot on a peripheral card interconnect bus,

the blinking pattern to indicate the command being processed.

2. The apparatus of claim 1, wherein the command is to turn the light emitting diode “on,” “off,” or make the diode blink in a blinking pattern having a duty cycle of approximately fifty percent.

3. The apparatus of claim 1, wherein the command is to apply power only to at least one target peripheral card interconnect slot, to enable at least one target peripheral card interconnect slot, to disable at least one target peripheral card interconnect slot, or to change a speed of the peripheral card interconnect bus.

4. An apparatus, comprising:

a standard hot-plug controller, the standard hot-plug controller having a register for receiving a command from a microprocessor, the standard hot-plug controller to:

cause execution of a blinking pattern on at least one light emitting diode associated with at least one target peripheral card interconnect slot on a peripheral card interconnect bus, and

the blinking pattern to indicate an error occurring during processing of the command.

5. The apparatus of claim 4, wherein the blinking pattern is to indicate an error occurring before power is applied to the target slot.

6. The apparatus of claim 4, wherein the blinking pattern is to indicate a hard error or a soft error.

7. The apparatus of claim 4, wherein the blinking pattern is to indicate an error occurring after power is applied to the target slot.

8. The apparatus of claim 4, wherein the blinking pattern has a duty cycle that is less than or greater than approximately fifty percent.

9. A method, comprising:

receiving a command at a standard hot-plug controller from a microprocessor; and

causing execution of a blinking pattern on at least one light emitting diode associated with at least one target peripheral card interconnect slot on a peripheral card interconnect bus,

the blinking pattern indicating the command being processed,

the blinking pattern having a duty cycle that is less than or greater than approximately fifty percent.

10. The method of claim 9, further comprising receiving a command to turn the light emitting diode “on,” “off,” or make the diode blink in a blinking pattern having a duty cycle of approximately fifty percent.

11. The method of claim 9, further comprising receiving a command to apply power to the target peripheral card interconnect slot, to enable the target peripheral card interconnect slot, to disable the target peripheral card interconnect slot, or to change the speed of the peripheral card interconnect bus.

12. A method, comprising:

receiving at least one command at a standard hot-plug controller from a microprocessor; and

causing execution of a blinking pattern on at least one light emitting diode associated with at least one target peripheral card interconnect slot on a peripheral card interconnect bus,

the blinking pattern indicating an error occurring during processing of the command.

13. The method of claim 12, wherein the blinking pattern is indicating an error occurring before power is applied to the target slot.

14. The method of claim 12, wherein the blinking pattern is indicating a hard error or a soft error.

15. The method of claim 12, wherein the blinking pattern is indicating an error occurring after power is applied to the target slot.

16. The method of claim 12, wherein the blinking pattern includes a duty cycle that is less than or greater than approximately fifty percent.

17. An article of manufacture including a machine-accessible medium having data that, when accessed by a machine, cause the machine to perform the operations comprising:

receiving a command at a standard hot-plug controller from a microprocessor; and

causing execution of a blinking pattern on at least one light emitting diode associated with at least one target peripheral card interconnect slot on a peripheral card interconnect bus,

the blinking pattern indicating the command being processed,

the blinking pattern having a duty cycle that is less than or greater than approximately fifty percent.

18. The article of manufacture of claim 17, wherein the machine-accessible medium further includes data that cause the machine to perform operations comprising receiving a command to turn the light emitting diode “on,” “off,” or make the diode blink in a blinking pattern having a duty cycle of approximately fifty percent.

19. The article of manufacture of claim 17, wherein the machine-accessible medium further includes data that cause the machine to perform operations comprising receiving a command to apply power to the target peripheral card interconnect slot, to enable the target peripheral card interconnect slot, to

disable the target peripheral card interconnect slot, or to change the speed of the peripheral card interconnect bus.

20. An article of manufacture including a machine-accessible medium having data that, when accessed by a machine, cause the machine to perform the operations comprising:

receiving at least one command at a standard hot-plug controller from a microprocessor; and

causing execution of a blinking pattern on at least one light emitting diode associated with at least one target peripheral card interconnect slot on a peripheral card interconnect bus, and

the blinking pattern indicating an error occurring during processing of the command.

21. The article of manufacture of claim 20, wherein the machine-accessible medium further includes data that cause the machine to perform operations comprising indicating an error occurring before power is applied to the target slot.

22. The article of manufacture of claim 20, wherein the machine-accessible medium further includes data that cause the machine to perform operations

comprising causing execution of the blinking pattern at a duty cycle that is less than or greater than approximately fifty percent.

23. The article of manufacture of claim 20, wherein the machine-accessible medium further includes data that cause the machine to perform operations comprising indicating an error occurring after power is applied to the target slot.

24. The article of manufacture of claim 20, wherein the machine-accessible medium further includes data that cause the machine to perform operations comprising indicating a hard error or a soft error.

25. A system, comprising:

a peripheral component interconnect bus having at least one peripheral component interconnect slot thereon, the peripheral component interconnect slot having at least one light emitting diode associated therewith,

a bridge coupled to the peripheral component interconnect bus, the bridge having a standard hot-plug controller coupled to the peripheral component interconnect bus, the standard hot-plug controller to receive a command from a microprocessor, and cause execution of a blinking pattern on at least one light emitting diode, the blinking pattern to indicate the command being processed, the blinking pattern having a duty cycle that is less than or greater than approximately fifty percent.

26. The system of claim 25, further comprising a memory coupled to the bridge.

27. The system of claim 26, wherein the memory is a static random access memory (SRAM).

28. A system, comprising:

a peripheral component interconnect bus having at least one peripheral component interconnect slot thereon, the peripheral component interconnect slot having at least one light emitting diode associated therewith,

a bridge coupled to the peripheral component interconnect bus, the bridge having a standard hot-plug controller coupled to the peripheral component interconnect bus, the standard hot-plug controller to receive a command from a microprocessor, and cause execution of a blinking pattern on at least one light emitting diode, the blinking pattern to indicate an error occurring during processing of the command, the blinking pattern having a duty cycle that is less than or greater than approximately fifty percent.

29. The system of claim 28, further comprising a memory coupled to the bridge.

30. The system of claim 29, wherein the memory is a static random access memory (SRAM).